



# Ingens

A Unit of [Ensemble Tech Pvt Ltd](#)

IEEE Projects in - Embedded Sys | VLSI | DSP | DIP | EIE | MATLAB | Electrical | Android

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## About Us :

We at Ingens Tech specialize in academic projects training, guidance and implementation. We offer project guidance and training for final year projects in departments like ECE, EEE, E&I and other associated departments. We have a impeccable track record over the last ten years, during which we have guided thousands of projects in various domains like

- Embedded System Projects
- VLSI Projects
- DSP Projects
- DIP Projects
- MATLAB Projects
- Electrical Projects
- Instrumentation Projects
- Robotics Projects



...and many more related domains. Most of these projects are based on latest international publications like IEEE papers. We hand pick IEEE projects for students to meet their requirements.

## About This List :

This projects list is a partial list, taken from our full projects list for the year 2013-14. Most of the projects on the list are based on IEEE base papers for 2013-14. This list is only to give the students a brief idea about the possibilities with a specific technology. We have 100s of other projects in various other domains also. Students can choose either from this list or contact us to get more project options.

## Contact Us :

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S.No	VLSI   FPGA   CPLD   VHDL   Verilog	CODE
1	Precision Control of Modular Robot Manipulators: The VDC Approach With Embedded FPGA	INVLS01
2	FPGA-Based Predictive Sliding Mode Controller of a Three-Phase Inverter	INVLS02
3	An Improved FPGA Implementation of Direct Torque Control for Induction Machines	INVLS03
4	CW and Pulse–Doppler Radar Processing Based onFPGA for Human Sensing Applications	INVLS04
5	Simple Digital Pulse Width Modulator Under 100 ps Resolution Using General-Purpose FPGAs	INVLS05
6	Architecture Exploration for a High-Performance and Low-Power Wireless Vibration Analyzer	INVLS06
7	Embedding Synchronized Measurement Technology for Smart Grid Development	INVLS07
8	Control Strategy for Power Flow Management in a PV System Supplying DC Loads	INVLS08
9	A novel FPGA educational paradigm using the next generation programming languages case of an embedded FPGA system course	INVLS09
10	Fast FPGA-based fault injection tool for embedded processors	INVLS10
11	From gates to FPGA: Learning digital design with Deeds	INVLS11
12	An FPGA co-processor for adaptive lane departure warning system	INVLS12
13	FPGA Based Elevator Controller with Improved Reliability	INVLS13
14	An FPGA based implementation of a flexible digital PID controller for a motion control system	INVLS14
15	FPGA based control for real time systems	INVLS15
16	Internet-controlled dynamic reconfiguration forFPGA-based embedded systems	INVLS16
17	FPGA based 2-stage power conditioning system for PV power generation	INVLS17
18	A MEMS integrated navigation system based on DSP and FPGA	INVLS18
19	Fault tolerant CAN bus control system implemented into FPGA	INVLS19
20	Implementation of I2C master bus controller onFPGA	INVLS20
21	Micropositioning mechatronics system based onFPGA architecture	INVLS21



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22	Design and implementation of a secure RFID system on FPGA	INVLS22
23	Real-time bluetooth communication between anFPGA based embedded system and an Android phone	INVLS23
24	RFID based navigation system for unmanned material handling vehicles using FPGA	INVLS24
25	Development of Frequency Variable Inverter Based on SOPC and Nios II	INVLS25
26	A Hardware Approach for Solving the Robot Localization Problem Using a Sequential EKF	INVLS26
27	Embedded Motion Controller Design Based on RTEX Network	INVLS27
28	Creating a programming language for the AL5 type robotic arms	INVLS28
29	A novel digital controller for microstepping stepper motor drive using FPGA for Solar Array Drive Assembly in satellites-A comparison With alternative schemes	INVLS29
30	A new high-efficiency stepper motor driver for old technology stepper motors	INVLS30
31	Design and implementation of fuzzy-logic based obstacle-avoidance and target-reaching algorithms on NI's embedded-FPGA robotic platform	INVLS31
32	Hardware amount evaluation of the improved WEP on an FPGA	INVLS32
33	Using remote update controller of FPGA's as built-in self test for embedded systems	INVLS33
34	Using FPGA for real time power monitoring in a NIALM system	INVLS34
35	SENTIOF: An FPGA based high-performance and low-power wireless embedded platform	INVLS35