



A Unit of Ensemble Tech Pvt Ltd

IEEE Projects in - Embedded Sys | VLSI | DSP | DIP | EIE | MATLAB | Electrical | Android

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About Us :

We at Ingens Tech specialize in academic projects training, guidance and implementation. We offer project guidance and training for final year projects in departments like ECE, EEE, E&I and other associated departments. We have a impeccable track record over more than ten years, during which we have guided thousands of projects in various domains like

- Embedded System Projects
- VLSI Projects
- DSP Projects
- DIP Projects
- MATLAB Projects
- Electrical Projects
- Instrumentation Projects
- Robotics Projects



...and many more related domains. Most of these projects are based on latest international publications like IEEE papers. We hand pick IEEE projects for students to meet their requirements.

About This List :

This projects list is a partial list, taken from our full projects list for the year 2014-15. Most of the projects on the list are based on IEEE base papers for 2014-15. This list is only to give the students a brief idea about the possibilities with a specific technology. We have 100s of other projects in various other domains also. Students can choose either from this list or contact us to get more project options.

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S.No	Code	VLSI / FPGA / SOC / NIOS 2
1	IGVLS01	A highly integrated hardware design implemented on FPGA for a wireless healthcare monitoring system
2	IGVLS02	Design of an FPGA based visible light communication system
3	IGVLS03	The remote control system of the manipulator
4	IGVLS04	Remote access of FPGA robot via internet
5	IGVLS05	Implementation of Shortest path planning algorithm without track using FPGA robot: A new approach
6	IGVLS06	Fast contactless vibrating structure characterization using real time field programmable gate array-based digital signal processing: Demonstrations with a passive wireless acoustic delay line probe and vision
7	IGVLS07	FPGA implementation of rapid PN code acquisition using iterative message passing algorithms
8	IGVLS08	Design and Implementation of Sensorless Capacitor Voltage Balancing Control for Three-Level Boosting PFC
9	IGVLS09	A Self-Powered CMOS Reconfigurable Multi-Sensor SoC for Biomedical Applications
10	IGVLS10	A Novel Reduced Switching Loss Bidirectional AC/DC Converter PWM Strategy With Feedforward Control for Grid-Tied Microgrid Systems
11	IGVLS11	Sonar inside your body: Prototyping ultrasonic intra-body sensor networks
12	IGVLS12	Design of high performance system-on-chips using Field Programmable Gate Arrays (FPGA)
13	IGVLS13	Multifunctional Controller Architecture for Solid-State Marx Modulator Based on FPGA
14	IGVLS14	New topology three phase multilevel inverter for grid-connected photovoltaic system
15	IGVLS15	Horizontal velocity estimation via downward looking descent images for lunar landing
16	IGVLS16	Characteristics of baseband digital signal transmission for intrabody communications
17	IGVLS17	Versatile modular electronics for rapid design and development of humanoid robotic subsystems

18	IGVLS18	Using FPGA to control a virtual sorting system
19	IGVLS19	Voltage and Current-Mode Control for a Buck-Converter based on Measured Integral Values of Voltage and Current Implemented in FPGA
20	IGVLS20	Analysis and Implementation of FPGA-Based Online Parametric Identification Algorithms for Resonant Power Converters
21	IGVLS21	A Reconfigurable Smart Sensor Interface for Industrial WSN in IoT Environment
22	IGVLS22	Energy-aware intelligent controller for Dynamic Energy Management on smart microgrid
23	IGVLS23	LabVIEW based remote laboratory for advanced motion control
24	IGVLS24	A Multicore Architecture for High-Performance Scientific Computing Using FPGAs
25	IGVLS25	Modelling and design of cost efficient novel digital controller for brushless DC motor drive
26	IGVLS26	Fuzzy logic-based hardware architecture for event detection in Wireless Sensor Networks
27	IGVLS27	Towards Smart Integration of Distributed Energy Resources Using Distributed Network Protocol Over Ethernet
28	IGVLS28	Turnkey Solution for Single-phase Grid-connected DC/AC Converter Controls
29	IGVLS29	A novel methodology to increase fault tolerance in autonomous FPGA-based systems
30	IGVLS30	Performance and control system design for FPGA based CVMPPPT boost converter for remote SPV water pumping system applications
31	IGVLS31	Distributed process monitoring and control using FPGA
32	IGVLS32	Fixed-point computation of robot kinematics in FPGA
33	IGVLS33	FPGA-Based Bit Error Rate Performance Measurement of Wireless Systems
34	IGVLS34	FPGA-based real time incremental conductance maximum power point tracking controller for photovoltaic systems
35	IGVLS35	Load Sharing Strategy for Autonomous AC Microgrids Based on FPGA Implementation of ADALINE&FLL
36	IGVLS36	Run-time power gating in hybrid ARM-FPGA devices
37	IGVLS37	An embedded robot controller based on ARM and FPGA
38	IGVLS38	Prediction of Power Supply Noise From Switching Activity in an FPGA



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39	IGVLS39	Application of rapid prototyping tools for a hierarchical microgrid control implementation
40	IGVLS40	ASIC and FPGA Implementation of the Gaussian Mixture Model Algorithm for Real-Time Segmentation of High Definition Video
41	IGVLS41	Design and implementation of power flow control for a novel dual input DC-DC converter
42	IGVLS42	Scalable Digital Power Controller With Phase Alignment and Frequency Synchronization
43	IGVLS43	Infrared beacon based sub-meter indoor localization
44	IGVLS44	FPGA Implementation of Software Defined Radio Based Flight Termination System
45	IGVLS45	An Event-Driven Approach to the Current Control of a BLDC Motor Using FPGA
46	IGVLS46	Supervisory control for interleaved boost converters using HiLeS-designer
47	IGVLS47	Graphic technologies for virtual, remote and hybrid laboratories: WebLab-FPGA hybrid lab
48	IGVLS48	PLL control system based on FPGA for brushless DC motor
49	IGVLS49	Bridging the gap between hardware and software open source network developments
50	IGVLS50	Home automation system based on FPGA and GSM
51	IGVLS51	“The Novel Technique For Channel Security using UART”
52	IGVLS52	Design and deployment of reconfigurable hardware using Web Services
53	IGVLS53	Modelling and effective predictive control of gas turbine process
54	IGVLS54	Implementation and testing of optimal design of RTU hardware for Wireless SCADA
55	IGVLS55	Dimmable AC LED Driver With Efficiency Improved Based on Switched LED Module
56	IGVLS56	Raspberry Pi as a measurement system control unit
57	IGVLS57	Remote access of FPGA robot via internet
58	IGVLS58	Timestamping and Ranging Performance for IEEE 802.15.4 CSS Systems
59	IGVLS59	FPGA-Based Design of Grid Friendly Appliance Controller
60	IGVLS60	Real-Time Visual Saliency Architecture for FPGA With Top-Down Attention Modulation
61	IGVLS61	Home automation system based on FPGA and GSM
62	IGVLS62	Field-Programmable System-on-Chip for Localization of UGVs in an Indoor iSpace
63	IGVLS63	DART: A Programmable Architecture for NoC Simulation on FPGAs



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64	IGVLS64	A highly parallelized control system platform architecture using multicore CPU and FPGA for multi-DoF robots
65	IGVLS65	Real-Time Embedded Implementation of the Binary Mask Algorithm for Hearing Prosthetics
66	IGVLS66	Prototype of an embedded system using Stratix III FPGA for vehicle detection and traffic management
67	IGVLS67	Time-Interleaved $\Sigma\Delta$ Modulators for FPGAs
68	IGVLS68	Implementation of WSN which can simultaneously monitor temperature conditions and control robot for positional accuracy
69	IGVLS69	FPGA-based implementation of an adaptive P&O MPPT controller for PV applications
70	IGVLS70	A software defined wireless sensor network
71	IGVLS71	A novel bit stuffing technique for Controller Area Network (CAN) protocol
72	IGVLS72	FPGA implementation of advanced health care system using Zig-Bee enabled RFID technology
73	IGVLS73	Design and Implementation of FPGA - digital based PID controller
74	IGVLS74	Vehicle-to-Vehicle Propagation Models With Large Vehicle Obstructions
75	IGVLS75	On the Accuracy of Digital Phase Sensitive Detectors Implemented in FPGA Technology
76	IGVLS76	A Tool to Analyze Potential I/O Attacks against PCs
77	IGVLS77	Remote reconfigurable wireless sensor node design for Wireless Sensor Network
78	IGVLS78	A Multi-Resolution FPGA-Based Architecture for Real-Time Edge and Corner Detection
79	IGVLS79	Design and Smooth Position/Force Switching Control of a Miniature Gripper for Automated Microhandling
80	IGVLS80	Next generation visible light communications: 10 Mb/s with polymer light-emitting diodes
81	IGVLS81	Implementation of real time moving object detection using background subtraction in FPGA
82	IGVLS82	Asymmetry Mitigation in IEEE 802.3 Ethernet for High-Accuracy Clock Synchronization
83	IGVLS83	AFS design on FPGA for automobiles
84	IGVLS84	Implementation of Wireless Sensor Network Using Microblaze and Picoblaze Processors
85	IGVLS85	Flexible platform for coding evaluation in narrowband power line communication



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86	IGVLS86	Design and FPGA Implementation of High-Speed, Fixed-Latency Serial Transceivers
87	IGVLS87	A reconfigurable overlapping FFT/IFFT filter for ECG signal de-noising
88	IGVLS88	Hardware stream cipher with controllable chaos generator for colour image encryption
89	IGVLS89	Embedded System for Biometric Online Signature Verification
90	IGVLS90	Pseudo-Sensorless High-Performance Bilateral Teleoperation by Sliding-Mode Control and FPGA
91	IGVLS91	FPGA-based design of a step-up photovoltaic array emulator for the test of PV grid-connected inverters